

GP 2814



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Docket: 0756-1982

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)

Shunpei YAMAZAKI)

Group Art Unit: 2814

Serial No. 09/327,469)

Examiner: G. PERALTA

Filed: June 8, 1999)

For: SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING
THE SAME)

Commissioner for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 8-17-00

Sir:

Transmitted herewith is an Amendment and Response to Restriction Requirement in the above-identified application.

The fee has been calculated as shown below:

	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Small/Large Entity Rate	Fee
Total	51	Minus	55	-0-	x \$9/\$18	\$0.00
Indep.	9	Minus	11	-0-	x \$39/\$78	\$0.00
First Presentation of Multiple Dependent Claim					+ \$130/\$260	\$0.00
TOTAL						\$0.00

[] A check in the amount of \$ _____ to cover the additional claims and extension fee is enclosed.

[X] In the event applicant(s) has overlooked the need for any petition and

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fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.

- [X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



Eric J. Robinson
Reg. No. 38,285

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AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action of July 14, 2000, please amend the subject
application as follows:

IN THE CLAIMS:

Please cancel non-elected claims 14-19 without prejudice to Applicants' right to file
a divisional application with respect thereto.

Please add the following new claims:

--20. A method of manufacturing a semiconductor device comprising the steps of:
forming an amorphous semiconductor film on an insulating surface;
providing said semiconductor film with a crystallization promoting material
comprising a metal;

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